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# **1** Introduction

## **1 Overview**

RecaData's PATA SSD (Solid State Drive) is a high performance and high reliability storage device based on NAND Flash technology that designed to solve the bottleneck of computing system by traditional hard disk drives. RecaData 's PATA SSD doesn't have a moving parts and it has a same host interface and same physical dimension with Hard Disk Drive,So it can be drop-in replaced with the hard disk drives without anything. With a high performance and low power consumption, RecaData 's PATA SSD can be a good storage device for NB and Tabletop PC.

RecaData 's PATA SSD purely consists of semiconductor devices and NAND flash memories, which give rugged features against shock and vibration use in extreme environment such as industrial PC an increased MTBF. Further more, RecaData 's PATA SSD has highly advanced flash memory management algorithm to guarantee higher performance and data integrity.

## **2 Part Type Introduction**

This chapter is about the specifications of the RecaData 2.5"PATA Solid State Drive SSD with IDE 44Pin interface.

Product Type	Capacity	interface	
RCI-P25008-MS	8G	IDE 44Pin	
RCI-P25016-MS	16G	IDE 44Pin	
RCI-P18032-MS	32G	IDE 44Pin	
RCI-P18064-MS	64G	IDE 44Pin	
RCI-P18128-MS	128G	IDE 44Pin	
RCI-P25256-MS	256G	IDE 44Pin	

Table 1 Capacity Specifications

# 2 Outline

	Interface IDE 44Pin			
	Dimension	100x70x9.5 mm		
Based spec	Weight(1)	72~82 g		
	Capacity	8G~256G		
	Flash type	NAND Flash		
	Sequential Read	75MB/S		
	Sequential Write	45MB/S		
Read/Write Performance②	4KB Random Read IOPS	4000		
	4KB Random Write IOPS	500		
	Response Time	0.2ms		
	Power Supply	_5V <u>+</u> 5%		
Power Consumption	Standby	0.5W		
	4KB Random Write	2W		
Reliability	Write endurance: 20 years @ 500G write/day(32G)			
	Read endurance: unlimited			
	MTBF: >2,000,000 hours			
	Data retention: >20years @ 25℃			
	Data destroy do not support			
	Sudden power-off recovery suppor	t		
	ECC: 8bits or 15bits for 512KBytes			
	S.M.A.R.T and dynamic power man	agement support		
	Static and dynamic wear-leveling			
	Bad block management algorithm			
	Storage temperature: -55~105 °C			
	Operation temperature: -40~85 °C			
Environment	Humidity: 5%~95%			
	Vibration: 20G Peak, 10 ~ 2000Hz, (15mins/ Axis) x3 Axis			
	Shock: 1500G (@0.5ms half sine wa	ave)		
Warranty	2 years			

Table 2 outline of the driver

(1,2): The Read/Write performance and weight vary with different capacity of products.

②:The testing environment is below:

OS: Microsoft Windows XP professional SP3 CPU: Intel Pentium® Dual-Core E5300 @2.6GHz Memory: 2GB Motherboard: GIGABYTE GA-G41M-ES2L Test program: HDspeed(sequential R/W speed) IOmeter (IOPS) HDtune(response time)

Test Drive : RCI-P25128-MS (MLC)

# **3 Block Diagram**



# **4 Product Specifications**

# 4.1 Physical dimensions

Parameter	Value
Length	100 <u>+</u> 0.1 mm
width	70 <u>+</u> 0.1 mm
height	9.5 <u>+</u> 0.1 mm

Table 3 Physical dimensions of the driver

## 4.2 Interface Specification

4.2.1 Pin assignment						
Pin number	signal	Pin type	Pin number	signal	Pin type	
1	RESET#		2	GND	Ground	
3	D7	I/O	4	D8	I/O	
5	D6	I/O	6	D9	I/O	
7	D5	I/O	8	D10	I/O	
9	D4	I/O	10	D11	I/O	
11	D3	I/O	12	D12	I/O	
13	D2	VO VI	14	D13	I/O	
15	D1	1/0	16	D14	I/O	
17	D0	I/O	18	D15	I/O	
19	GND	Ground	20			
21	DMARQ		22	GND	Ground	
23	IOW#		24	GND	Ground	
25	IOR#		26	GND	Ground	
27	IORDY	0	28	CSEL	No connection	
29	DMACK		30	GND	Ground	
31	INTRQ	0	32	IOCS16#	0	
33	A1	1	34	PDIG#	I/O	
35	A0	1	36	A2	Ι	
37	CS0#	1	38	CS1#		
39	ACTIVE	I/O	40	GND	Ground	
41	NC	No connection	42	VCC	Power	
43	GND	Ground	44	TYPE#	No connection	

Table 4 IDE 44Pin interface Pin assignment

4.2.2 Master/Slave setting



Figure 2 Master/Slave setting

#### 4.2.3 Interface Mode

The interface of RecaData's 2.5"PATA complies with the ATA-7 standard ①Host Transfer Rate is 150MB/s(1.5Gb/s)

Transfer modes:

PIO mode 0,1,2,3,4
 DMA mode 0,1,2

③UDMA mode 0,1,2,3,4,5,6

# **5** Electrical Characteristics

## 5.1 Absolute Maximum Rating

Parameter	Symbol	Condition	Min	Max	Unit
TSGT	Storage temperature		-55	140	°C
AVDDH	Analog Power supply		-0.5	6	V
DVDD	Digital I/O Power Supply		-0.5	6	V
V <sub>I(D)</sub>	Digital I/O Input Voltage		-0.4	DVDD+0.4	V

Table 5 absolute maximum rating

### 5.2 Recommended Power Supply Operation Conditions

	~		7 - 7			
Parameter	Symbol	Condition	Min	Typical	Max	Unit
Operation disitel power upply	PV33		3.0	3.3	3.6	V
Operation digital power supply	D1V8		1.85	1.9	2.0	V
Operation analog power supply	ASV33		3.0	3.3	3.6	V
	ASV18		1.85	1.9	2.0	V
	AVDDH		3.0	3.3	3.6	V
Ambient operation temperature	TA	For commercial	0		70	ç
Ambient operation temperature	T	For Industrial	-40		85	ĉ
Junction temperature	L		0		125	ĉ

Table 6 recommended power supply operation conditions

# **5.3 Recommended External Clock Conditions**

Parameter	Symbol	Condition	Min	Typical	Max	Unit
External reference clock				30		MHz
Clock duty cycle			45	50	55	%

Table 7 recommended external clock conditions

## 5.4 I/O DC Characteristics

Parameter	Symbol	Condition	Min	Typical	Max	Unit
Input low voltage	V⊩				0.8	V
Input high voltage	VHL		2.0			V
Output low voltage	Vol		0		0.4	V
Output high voltage	Vон		2.6		0.36	V

Table 8 I/O DC characteristics

# **6 Product trait**

# 6.1 Environment Specification

tomporaturo	Operation	<b>-20~70</b> ℃
temperature	Operation	<b>0~70°</b> ℃
humidity	5%~95%	20%~90%
vibration	20G(40~2000Hz)	
shock	1500G(@0.3ms half s	sine wave)
altitude	-8000~10000 foot	

Table 9 environment specifications

## 6.2 Power specification

Recommended DC operation voltage: 4.75~5,25V

-	-			
Param	eter	Typical	Unit	
Stand	lby	0.5	W	
Idle		0.5	W	
4KB sample data	Sequential read	1.15	W	
	Sequential write	1.0	W	
	Random read	2.0	W	
	Random write	2.0	W	
	Sequential read	1,15	W	
	Sequential write	1.3	W	
SIZKE sample data	2.0	W		
	Random write	2.0	W	

Table 10 power consumption

Note: 1.The power consumption can differ depending on the disk capacity and the type of flash memory being used.

- 2. The test drive : RMI-P25032-S
- 3. Supply voltage :5V<u>+</u>5%

# 6.3 Reliability Specification

> Wear-leveling

RecaData's SSD support both dynamic and static wear-leveling. These two algorithms grarantee all type of flash memory at same level of erase cycles to improve lifetime limitation of NAND based storage.

- Endurance
   Write edurance:8years @ 100G write/day (32G)
   Read endurance: unlimited
- > ECC

ECC(Error Correction Code):BCH ECC 8bits or 15 bits in 512Bytes

Bad block management algorithm

This algorithm replaces bad blocks with new ones from available spares on media error conditions

- MTBF MTBF(Mean Time Between Failures): >2,000,000 hours
   Data retention
  - Data retention:20years @25°C

# **7 Command Descriptions**

## 7.1 Support ATA Commands

Command name	Code(Hex)	Command name	Code(Hex)
CHECK POWER MODE	E5h	SECURITY ERASE UNIT	F4h
EXECUTE DIAGNOSTICS	90h	SECURITY FREEZE LOCK	F5h
FLUSH CACHE	E7h	SECURITY SET PASSWORD	F1h
IDENTIFY DEVICE	ECh	SECURITY UNLOCK	F2h
IDLE	E3h	SEEK	7xh
IDLE IMMEDIATE	E1h	SET FEATURES	EFh
INITIALIZE DEVICE PARAMETER	91h	SET MULTIPLE MODE	C6h
READ DMA	C8h or C9h	SLEEP	E6h
READ MULTIPLE	C4h	SMART	B0h
READ SECTOR(S)	20h or 21h	STANDBY	E2h
READ VERIFY SECTOR(S)	40h or 41h	STANDBY IMMEDIATE	E0h
RECALIBRATE	10h	WRITE DAM	CAh or CBh
SECURITY DISABLE PASSWORD	F6h	WRITE MULTIPLE	C5h
SECURITY ERASE PREPARE	F <u>3h</u>	WRITE SECTOR(S)	30h or 31h

Table 11 ATA commands list

## 7.2 ATA Commands Specifications

> CHECK POWER MODE(E5h)

The host can use this command determine the current power management mode **EXECUTE DIAGNOSTICS(90h)** 

- This command performs the internal diagnostic tests implemented by the drive.See error register for diagnostic mode
- FLUSH CACHE(E7h)

This command is used by the host to request the device to flush the write cache. If there is data in the write cache, that data shall be written to the media. The BSY bit should remain set to one until all data has been written or an error occurs.

> INDENTIFY DEVICE(Ech)

This commands read out 512Bytes of the drive parameter information.Parameter information consists of the arrangement and value as shown in the follow table.This command enables the host to receive the identify drive information from the device.

#### > IDLE(E3h)

This command causes the device set BSY, enter the Idle mode, clear BSY and generate an interrupt. If sectors is non-zero, the automatic power down mode is enable. If the sectors is zero, the automatic power down mode is disable.

#### > IDLE IMMEDIATE(E1h)

This command causes the device set BSY,enter the Idle(read) mode,clear BSY and generate an interrupt.

#### > INITIALIZE DEVICE PARAMETER(91h)

This command enables the host to set the number of sectors per track and the number of tracks per heads.

#### > READ DMA(C8h)

Read data from sectors during Ultra DMA and Multiword DMA transfer.Use the SET FEATURES command to specify the mode value. A sector count of zero requests 256 sectors.

#### READ MULTIPLE(C4h)

This command performs similarly to the Read Sector command.Interrupts are not generate on each sector,but on the transfer of a block which contains the number of sectors defined by a Set Multiple command.

#### READ SECTOR(S)(20h or 21h)

This command reads 1 to 256 sectors as specified in the sector count register from sectors which is set by Sector number register.A sector count of zero requests 256 sectors.The transfer being specified in the Ssector Nnumber register.

#### READ VERIFY SECTOR(S)(40h or 41h)

This command verifies one or more sectors on the device by transferring data from the flash media on the data buffer in the drive and verifying that the ECC is correct. This command is identical to the Read Sectors command, except that DRQ is never set and no data is transferred to the host.

#### RECALIBRATE(10h)

The current drive performs no processing if it receives this command.It's supported for backward compatibility with previous devices.

#### > SECURITY DISABLE PASSWORD(F6h)

Disable any previously set user password and cancels the lock.The host transfers 512Bytes data, as shown in the following table, to the device.The transferred data contains a user or master password, which the device compares with the saved password.If they match, the device cancels the lock.The master password is still saved.It's re-enabled issuing the SECURITY SET PASSWORD command to re-set a user password.

#### > SECURITY ERASE PREPARE(F3h)

This command shall be issued immediately before the Security Erase Unit command to enable erasing and unlocking. This command prevents accidental lost data on the device.

#### > SECURITY ERASE UNIT(F4h)

The host uses this command to transfer 512Bytes data, as shown in the following table, to the drive. The transferred data contains a user or master password, which the device compares with the saved password. If they match, the device delete the user data, disable the user password, and cancels the lock. The master password is still saved. It's re-enabled by issuing the SECURITY SET PASSWORD command to re-set a user password.

#### SECURITY FREEZE LOCK(F5h)

Caused the device enter FROZEN mode.Once this command has been executed,the following commands to update a lock result in the Aborted Command error:

- SECURITY SET PASSWORD
- SECURITU UNLOCK
- ♦ SECURITU DIABLE PASSWORD
- SECURITY ERASE PREPARE
- ♦ SECURITY ERASE UNIT

The drive exits from the FROZEN mode upon a power-off or hard reset. If the SECURITY FREEZE LOCK command issued when the device is placed in FROZEN mode, the drive execute the command, staying FROZEN mode.

#### SECURITY SET PASSWORD(F1h)

This command set user password or master password. The host outputs sector data with PIO data-out protocol to indicate the information defined in the following table.

#### SECURITY UNLOCK(F2h)

This command disabled LOCKED MODE of the device. This command transfers 512Bytes data from the host with PIO data-out protocol. The following table defines the content of this information.

#### > SEEK(7xh)

This command effectively a NOP command to the device although it dose perform a range check.

#### SET FEATURES(EFh)

This command set parameter to Features register and set drive!fls operation. For transfer mode, parameter is set to Sector Count register. This command is used by the host to establish or select certain features.

Value	Function
02h	Enable write cache
03h	Set transfer mode based on value in sector Count register
55h	Disable read look-ahead feature
82h	Disable write cache
AAh	Enable read look-ahead feature

 Table 12 Features register Value and settable operating mode

#### SET MULTIPLE MODE(C6h)

This command enables the device to perform READ MULTIPLE and WRITE MULTIPLE operations and establishes the block count of these commands.

#### > SLEEP(E6h)

This command causes the device to set BSY,enter the sleep mode,clear BSY and generation an interrupt.

#### > SMART(B0h)

Performs different processing required for predicting device failures, according to the subcommand specified in the Features register. If the Features register contains an unsupported value, the Aborted Command error is returned. If the SMART function is disabled, any subcommand other than SMART ENABLE OPERATIONS results in the Aborted Command error.

#### > STANDBY(E2h)

This command causes the device to set BSY, enter the Sleep mode (which corresponds to the ATA!Standby Mode), clear BSY and return the interrupt immediately.

STANDBY IMMEDIATE(E0h) This command causes the drive to set BSY, enter the Sleep mode (which corresponds to the ATA !Standby Mode), clear BSY and return the interrupt immediately.

#### WRITE DMA((CAh or CBh))

Write data to sectors during Ultra DMA and Multiword DMA transfer. Use the SET FEATURES command to specify the mode value.

#### > WRITE MULTIPLE(C5h)

This command is similar to the Write Sectors command. Interrupts are not presented on each sector, but on the transfer of a block which contains the number of sectors defined by Set Multiple command.

#### > WRITE SECTOR(S) (30h or 31h)

Write data to a specified number of sectors (1 to 256, as specified with the Sector Count register) from the specified address. Specify "00h"- to write 256 sectors

# 8 Part numbering scheme



Number	Character	meanings
1	R	RecaData Technology
2	С	Commercial
۷.	М	Military
		Industrial
3		First generation
	S	SATA
Δ	P	PATA
	M N	Mini PCIe SATA
	18	1.8 inch
5	25	2.5 inch
	50	50 mm
	004	4G
	008	8G
	016	16G
e v	032	32G
0	064	64G
	128	128G
	256	256G
	512	512G
7	S	SLC
ľ	М	MLC

Table 10 Part numbering scheme

# **9** Related documentation

For more information, visit www.recadata.com or contact supports@recadata.com.